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DATE: October 11, 2004

PTO IDENTIFIER: Application Number 10/653,237-Conf. #4842
Patent Number

Inventor: Katsuki Hazama

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FROM: CONNOLLY BOVE LODGE & HUTZ LLP

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Attorney Dkt. #: 20433-00601-US1

PAGES (Including Cover Sheet): 11

CONTENTS: Submission Of Omitted Reference (1 page);
Patent Abstracts of Japan, Publication No. 07-183411 (9 pages); and
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PTO/SB/97 (12-97)

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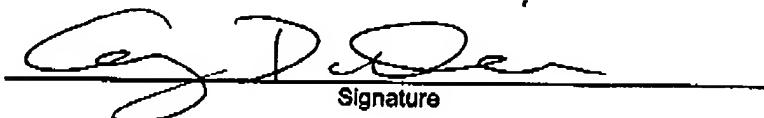
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Submission Of Omitted Reference (1 page);
Patent Abstracts of Japan, Publication No. 07-183411 (9 pages); and
Certificate of Transmission under 37 CFR 1.8 (1 page).

Docket No.: 20433-00601-US1
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Katsuki Hazama

Application No.: 10/653,237

Confirmation No.: 4842

Filed: September 3, 2003

Art Unit: 2822

For: SEMICONDUCTOR DEVICE INCLUDING
TRANSISTOR WITH COMPOSITE GATE
STRUCTURE AND TRANSISTOR WITH SINGLE
GATE STRUCTURE, AND METHOD FOR
MANUFACTURING THE SAME

Examiner: M. A. Wilczewski

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SUBMISSION OF OMITTED REFERENCE

Commissioner for Patents
P.O. Box 1450
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Dear Sir:

Further to our Amendment filing of October 10, 2004, please find attached the English translation of Japanese Publication No. 07-183411 which was erroneously not filed with the Amendment.

Applicant believes no fee is due with this response. However, if a fee is due, please charge our Deposit Account No. 22-0185, under Order No. 20433-00601-US1 from which the undersigned is authorized to draw.

Dated: October 11, 2004

Respectfully submitted,

By 
George R. Pettit

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